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REVISIONS

REV	DESCRIPTION	DATE	APPROVED
A	INITIAL RELEASE	26APR23	IANDAL

D

C

B

A

EVAL-ADXXXXX-SDZ SCHEMATIC REV (X)

PAGE 2 - XXXXX (E.G. POWER CIRCUITRY)

PAGE 3 - XXXXX (E.G. YOUR PART CIRCUITRY)



PAGE 4 - XXXXX

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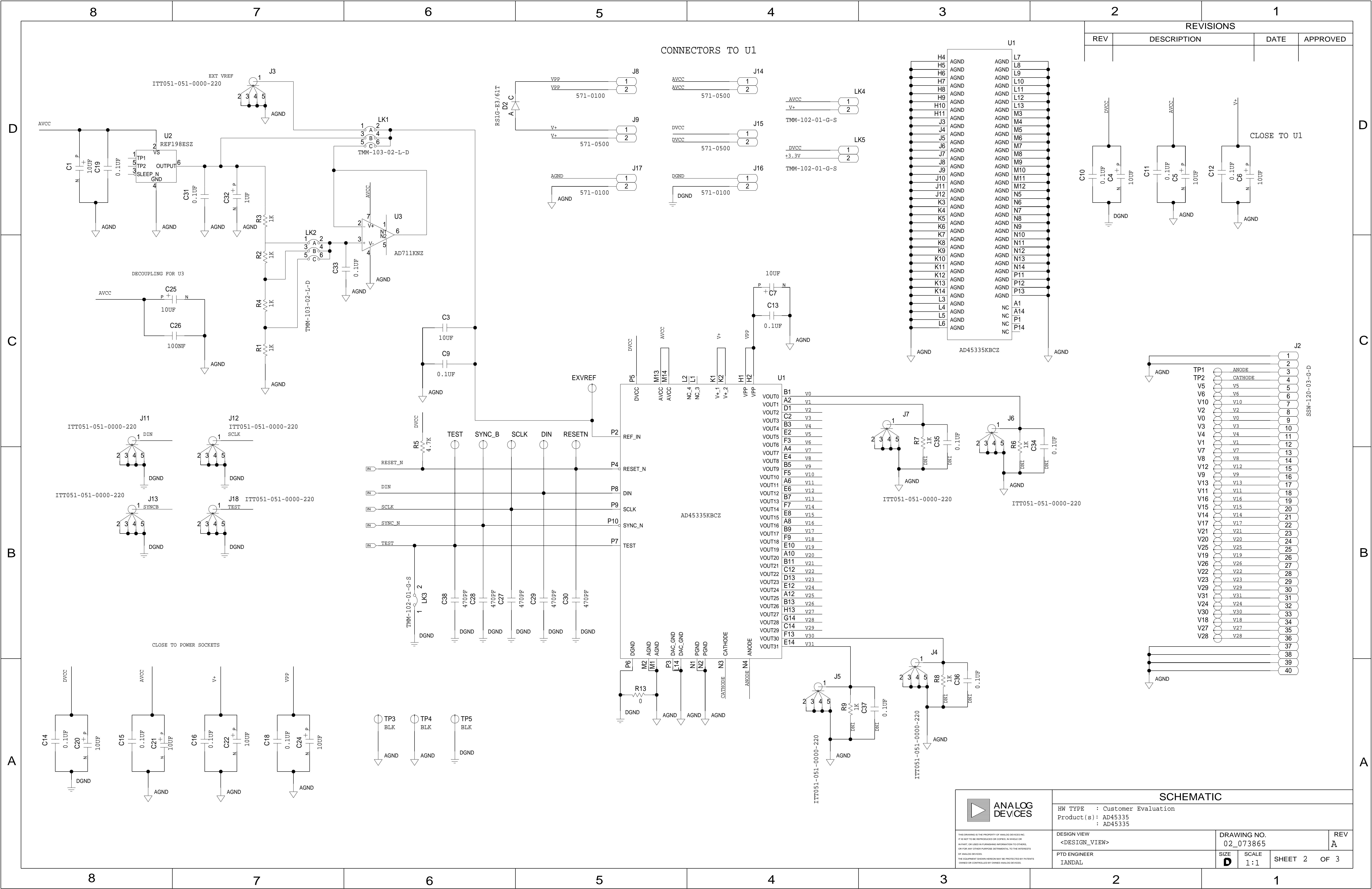
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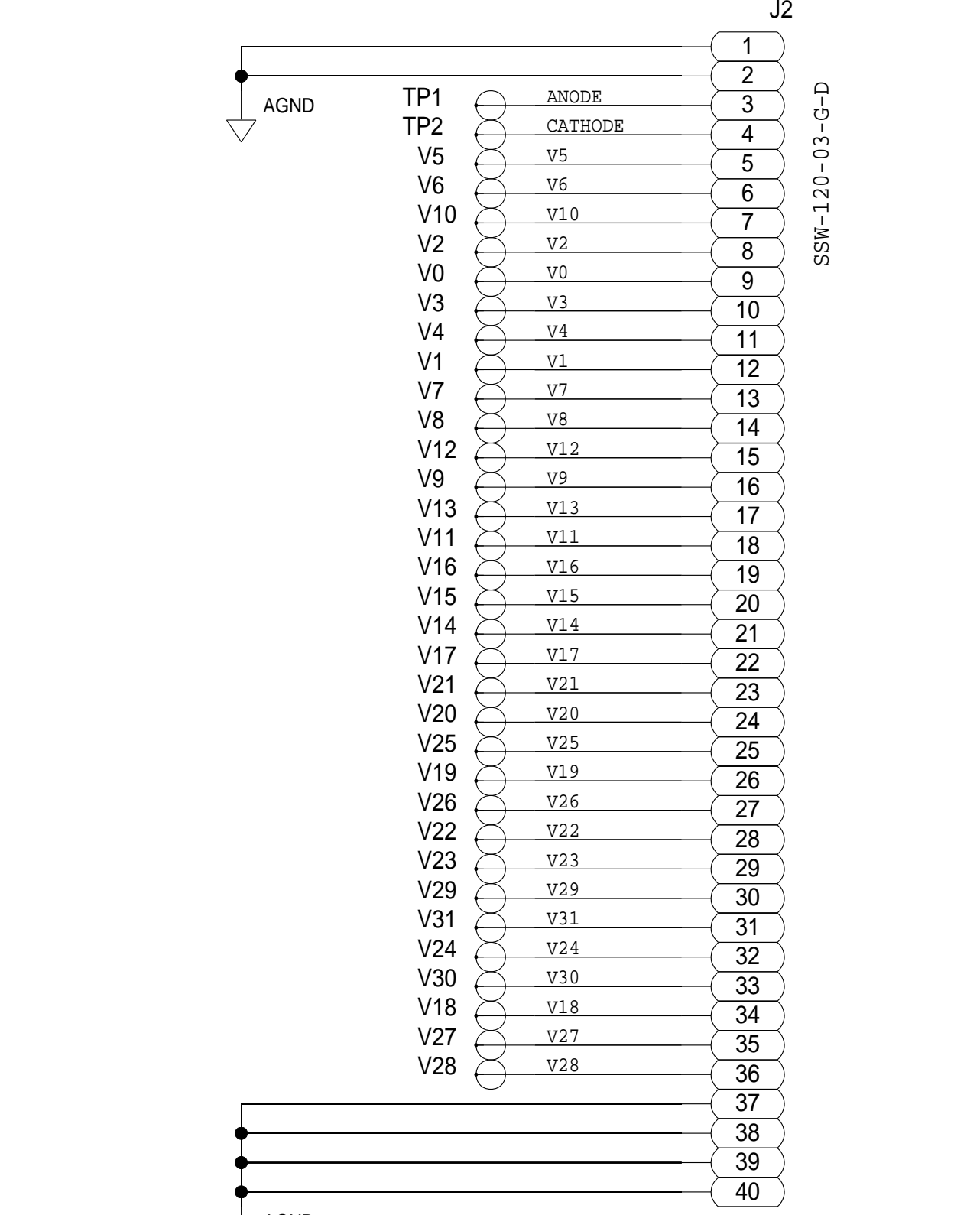
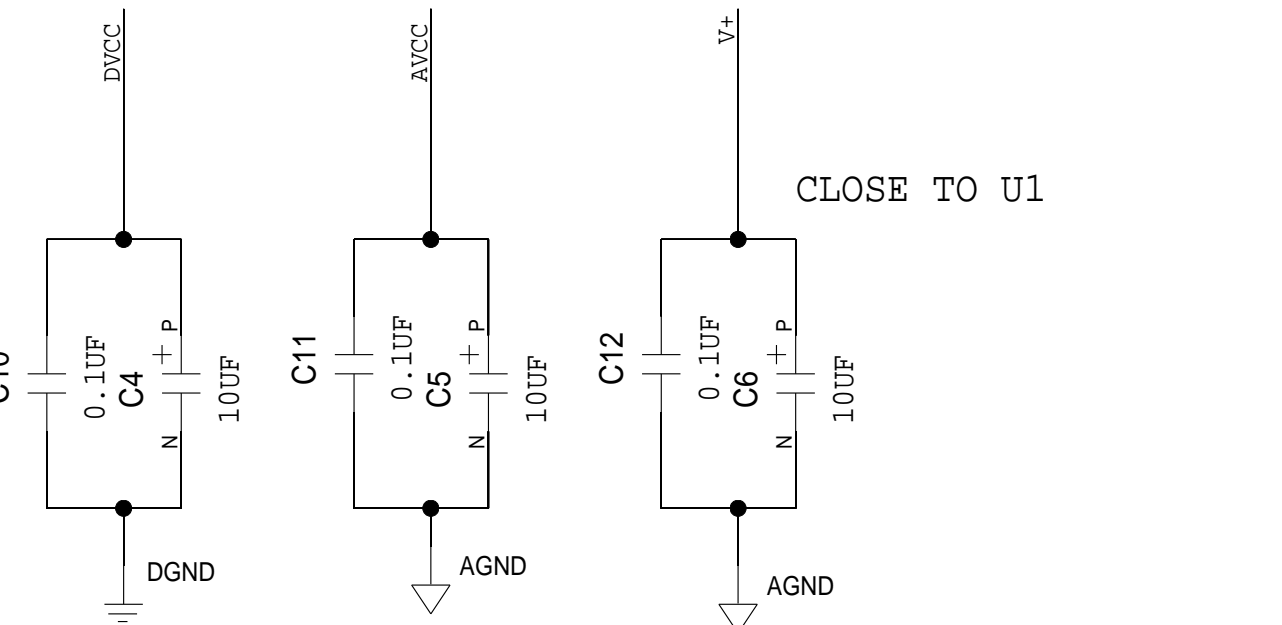
PAGE N - SDP CONNECTOR

TEMPLATE ENGINEER	DATE	SCHEMATIC		 		
HARDWARE SERVICES	26APR23					
HARDWARE SYSTEMS		HW TYPE : Customer Evaluation Product(s): AD45335 : AD45335 PACKAGE : 124-lead BodySize CSP_BGA-family : Pitch-pitch StyleVendor Style				
TEST ENGINEER						
COMPONENT ENGINEER	26APR23					
TEST PROCESS						
HARDWARE RELEASE	26APR23	<User Define> <User Define> <User Define>				
DESIGNER	26APR23	MASTER PROJECT TEMPLATE	TESTER TEMPLATE	DRAWING NO. 02_073865		REV. A
PTD ENGINEER	26APR23	TBD	-			
CHECKER		UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES		SIZE		
		TOLERANCES		SCALE		
		DECIMALS		CODE ID NO.		
		X.XX +0.010		CodeID		
		X.XXX +0.005		SHEET 1		
				OF 3		

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REVISIONS			
REV	DESCRIPTION	DATE	APPROVED



SCHEMATIC			
HW TYPE : Customer Evaluation Product(s) : AD45335 : AD45335			
DESIGN VIEW <DESIGN_VIEW>	DRAWING NO. 02_073865	REV A	
PTD ENGINEER IANDAL	SIZE D	SCALE 1:1	SHEET 2 OF 3

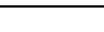
REVISIONS			
REV	DESCRIPTION	DATE	APPROVED

NOTE: THIS TEMPLATE WAS DESIGNED TO WORK WITH THE SDP-K1 AND TO BE BACKWARDS COMPATIBLE WITH THE SDP-S, SDP-B AND SDP-H1

USING THE RESISTORS CONNECTED TO THE ADDRESS PINS OF THE EEPROM YOU CAN SELECT THE I2C ADDRESS, AFTER SELECTING YOUR ADDRESS THE DNI RESISTORS SHOULD NOT BE REMOVED TO ENSURE YOU CAN CONNECT MULTIPLE BOARDS TO A SYSTEM. THE I2C ADDRESS NUMBER OF THIS EEPROM WILL BE IN THE RANGE OF 0X50 TO 0X57.

NOTE: CARE MUST BE TAKEN TO ENSURE DAUGHTER BOARD I2C EEPROM ADDRESS IS NOT 0X50 OR 0X51 THIS IS TO ENSURE THE I2C ADDRESS RANGE DOES NOT OVERLAP THAT OF THE EI3 BREAK-OUT BOARD.



 ANALOG DEVICES	<h1 style="text-align: center;">SCHEMATIC</h1>				
	HW TYPE : Customer Evaluation Product(s) : AD45335 : AD45335				
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	PTD ENGINEER IANDAL		SIZE D	SCALE 1:1	SHEET 3 OF 3